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Total Number of Pages in This Submission

Application Number	10/761,156
Filing Date	January 20, 2004
First Named Inventor	Stephen Paul Wilcox
Art Unit	2563
Examiner Name	Nelson Lam
Attorney Docket Number	104969-50921 (302715)

ENCLOSURES (Check all that apply)

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Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Gibbons, Del Deo, Dolan, Griffinger & Vecchione
Signature	
Printed name	David R. Padnes
Date	Aug. 3, 2006

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104969-50921 (302715)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Patent Application of:
Stephen Paul Wilcox**

Serial No.: 10/761,156

Filed: January 20, 2004

**For: Generation of Clock Gating Function For
Synchronous Circuit**

: Group Art Unit: 2563

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: Examiner: Nelson Lam

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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

Pursuant to the Office Action dated May 4, 2006 in connection with the above-identified application, please consider the following:

Remarks begin on page 2 of this paper.